# High Current Ferrite Chip Bead(Lead Free)

HFZ2012PV-700T60

|     |          | ECN HIST           | ORY LIST | Γ       |       |
|-----|----------|--------------------|----------|---------|-------|
| REV | DATE     | DESCRIPTION        | APPROVED | CHECKED | DRAWN |
| 1.0 | 22/11/16 | 新發行                | 鄧福興      | 浦冬生     | 王俞琴   |
| 2.0 | 22/12/05 | 更新可靠度及更正 Reflow 敘述 | 鄧福興      | 浦冬生     | 王俞琴   |
| 3.0 | 23/12/01 | 可靠度全面修訂為 REV E 版本  | 鄧福興      | 浦冬生     | 王俞琴   |
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TAI-TECH KBM01-241200400

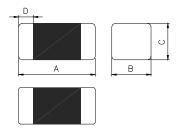
# High Current Ferrite Chip Bead(Lead Free)

HFZ2012PV-700T60

## 1. Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. High Current Bead Low RDC
- 6. Excellent solder ability and heat resistance.
- 7. High reliability. Reliability test meet AEC-Q200.
- 8. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.
- 10. Operating Temperature: -55~+150°C (Including self-temperature rise)

## 2. Dimensions



| A 2.00±0.20 |           |  |  |  |
|-------------|-----------|--|--|--|
| В           | 1.25±0.20 |  |  |  |
| С           | 0.85±0.20 |  |  |  |
| D           | 0.50±0.30 |  |  |  |

Units: mm

# 3. Part Numbering



A: Series

**B**: Dimension

C: Material

D: Category Code

E: Impedance

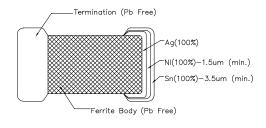
F: Packaging

Lead Free Material V=Vehicle **700=70** Ω

LxW

T=Taping and Reel, B=Bulk(Bags)

G: Rated Current 60=6000mA

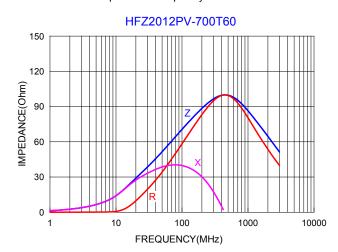


# 4. Specification

| Tai-Tech<br>Part Number | Impedance (()) |           | DC Resistance $(\Omega)$ max. | Rated Current (mA) max. |
|-------------------------|----------------|-----------|-------------------------------|-------------------------|
| HFZ2012PV-700T60        | 70±25%         | 60mV/100M | 0.009                         | 6000                    |

- Rated current: based on temperature rise test
- In compliance with EIA 595
- All test data referenced to 25<sup>°</sup>C ambient

Impedance-Frequency Characteristics



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# 5. Reliability and Test Condition

| Item                                  | Performance  | Test Condition  |
|---------------------------------------|--|---|
| Series No.                            | HFZ  |   |
| Operating Temperature                 | -55~+150℃<br>(Including self-temperature rise)   | -   |
| Transportation Storage Temperature    | -55~+150℃<br>(on board)  | For long storage conditions, please see the<br>Application Notice   |
| Impedance (Z)                         | Refer to standard electrical characteristics list  | Agilent4291<br>Agilent E4991<br>Agilent4287<br>Agilent16192   |
| DC Resistance  Rated Current          |  | Agilent 4338  DC Power Supply  Over Rated Current requirements, there will be some risk   |
| Temperature Rise Test                 | Rated Current < 1A ∆T 20°CMax Rated Current ≧ 1A ∆T 40°CMax  | Applied the allowed DC current.     Temperature measured by digital surface     Thermometer.  |
| High Temperature<br>Exposure(Storage) |  | Preconditioning:Run through reflow for 3 times.( IPC/JEDEC J-STD-020F Classification Reflow Profiles) Unpowered Temperature: 150±2°C Upper Temperature: maximum specified operating temperature (whichever is higher). Minimum test temperature shall be 85°C (For ferrite EMI suppressors/filters only) Duration : 1000hrs Min. Measured at room temperature after placing for 24±4 hrs  |
| Temperature Cycling                   | Appearance: No damage. Impedance: within±15% of initial value DCR: Within ±15% of initial value and shall not exceed the specification value | Preconditioning:Run through reflow for 3 times.( IPC/JEDEC J-STD-020F Classification Reflow Profiles Unpowered Lower Temperature of the Chamber: -40°C (For Inductors/Transformers) -55°C (For ferrite EMI suppressors/filters) Upper Temperature of the Chamber: maximum specified operating temperature (temperature and shall not exceed 125°C) Condition for 1 cycle Step1: -55±2°C 30min Min Step2: 125±2°C 30min Min. Step3: 125±2°C 30min Min. Step4: Dwell Time (Soak Time) 15 minutes minimum, 30 minutes minimum if component weighs above 28g Transition Time: 1 minute maximum Number of cycles: 1000 Measured at room temperature at least 24 hours after test conclusion. |
| Humidity Bias                         | Appearance: No damage. Impedance: within±15% of initial value DCR: Within ±15% of initial value and shall not exceed the specification value | Preconditioning:Run through reflow for 3 times.( IPC/JEDEC J-STD-020F Classification Reflow Profiles Unpowered(For Inductors/Transformers) Apply 10% of maximum rated power.( For ferrite EMI suppressors/filters ) Humidity :85±3%RH. Temperature :85±2°C. Duration :1000 hrs Min. Measured at room temperature after placing for 24±4 hrs   |

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| IAI-IECH                          |   | KBW01-241200400 P4   |  |  |  |  |
|-----------------------------------|---|--|--|--|--|--|
| Item                              | Performance   | Test Condition   |  |  |  |  |
| High Temperature Operational Life | Preconditioning: Run through Reflow (IPC/JEDEC J-STD-020F Classification (IPC/JEDEC J-STD-020F Classification Profiles) Temperature: 150±2°C Upper Temperature of the Chamber specified operating temperature (no rise) at maximum rated power and specified operating temperature (no rise) at maximum rated power and specified operating temperature (no rise) at maximum rated power and specified operating temperature of the Chamber maximal operating temperature of the Chamber maximal representation operating temperature up to 150°C. Suppressors/filters) Duration: 1000hrs Min. with 100% Measured at room temperature afte hrs Rated I <sub>L</sub> applied.(For ferrite EMI su |  |  |  |  |  |
| External Visual                   | Appearance : No damage.   | Inspect device construction, marking and workmanship. Pre and Post Electrical Test not required.   |  |  |  |  |
| Physical Dimension                | According to the product specification size measurement   | Verify physical dimensions to the applicable component detail specification. Pre and Post Electrical Test not required.  |  |  |  |  |
|                                   |   | Preconditioning:Run through reflow for 3 times.(IPC/JEDEC J-STD-020F Classification Reflow Profiles  Test condition:    Peak   Normal   Velocity   Velocit |  |  |  |  |
| Mechanical Shock                  |   | Type alue duration Wave form change (g's) (D) (ms) (Vi)ft/sec  |  |  |  |  |
|                                   |   | SMD         100         6         Half-sine         12.3           THT         100         6         Half-sine         12.3  |  |  |  |  |
|                                   | Appearance: No damage.  Impedance: within±15% of initial value  DCR: Within±15% of initial value and shall not exceed the specification value   | THT 100 6 Half-sine 12.3  3 shocks in each direction along 3 perpendicular axes (18shocks).  |  |  |  |  |
| Vibration                         |   | Preconditioning:Run through reflow for 3 times. (IPC/JEDEC J-STD-020F Classification Reflow Profiles Oscillation Frequency: 10Hz ~ 2KHz ~ 10Hz for 20 minute Equipment: Vibration checker Total Amplitude:5g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) •  |  |  |  |  |
|                                   |   | THT: Conditions B or C<br>Number of heat cycles: 1   |  |  |  |  |
|                                   |   | Solder technique simulation  Solder technique simulation  Test condition (°C)  Temperature Time ramp/immersion (s) and emersion rate   |  |  |  |  |
|                                   |   | Dip B (solder 10±1 25mm/s±6mm/<br>temp)  |  |  |  |  |
|                                   |   | Topside board-mount product C 260 ± 5 (solder temp) 20±1   |  |  |  |  |
|                                   |   | Depth: completely cover the termination  |  |  |  |  |
|                                   |   | SMD: Condition K, time above 217°C, 60s - 150s · Number of heat cycles:3   |  |  |  |  |
| Resistance to Soldering<br>Heat   | Appearance: No damage. Impedance: within±15% of initial value DCR: Within ±15% of initial value and shall not exceed the specification value  | Temperature  time 25°C to peak temperature  component  T <sub>Smax</sub> T <sub>L</sub> T <sub>smax</sub> T <sub>smax</sub> T <sub>smax</sub> T <sub>smax</sub> Tramp down  ramp up  reflow process  |  |  |  |  |
|                                   |   | Continental    Composet   Service    |  |  |  |  |

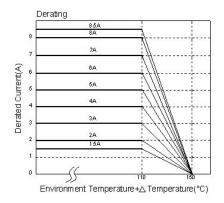
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| Item                           | Performance   | Test Condition  |
|--------------------------------|---|---|
| ESD                            | Appearance: No damage. Impedance: within±15% of initial value DCR: Within ±15% of initial value and shall not exceed the specification value  | Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target Test method: AEC-Q200-002 Test mode: Contact Discharge Discharge level: 4 KV (Level: 2)   |
| Solder ability                 | More than 95% of the terminal electrode should be covered with solder.  | Through-hole Technology (THT) Method A1, Coating Durability Category 2     SMD: Method B1, Coating Durability Category 2     Method D, Coating Durability Category 2     Magnification 50x     Pre and Post Electrical Test not required.     Non-soldered type mounting/attach are not applicable.   |
|                                |   | 浸入網爐角   |
| Electrical<br>Characterization | Refer Specification for Approval  | Parametrically test per lot and sample size requirements, (inductance only unless otherwise agreed upon) Summary to show minimum, maximum, mean and standard deviation at room, minimum and maximum operating temperatures. Pre and Post Electrical Test not required   |
| Board Flex<br>(SMD)            | Appearance: No damage. Impedance: within±15% of initial value DCR: Within ±15% of initial value and shall not exceed the specification value  Support  Solder Chip Printed circuit board before testing | Preconditioning: Run through Reflow for 3 times. (IPC/JEDEC J-STD-020F Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. |
| (Зив)                          | Probe to exert bending force  Radius 340  Primed circuit board under test  Displacement   |   |
| Terminal strength (SMD)        | Appearance: No damage. Impedance: within±15% of initial value DCR: Within±15% of initial value and shall not exceed the specification value   | Preconditioning: Run through Reflow for 3 times. (IPC/JEDEC J-STD-020F Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.  |

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## \*\*Derating Curve

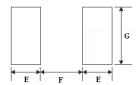
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $110^{\circ}$ C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



# 6. Soldering and Mounting

## 6-1. Recommended PC Board Pattern

|        | Chip Size |          |           |           |           |       | Pattern<br>ow Sold |       |
|--------|-----------|----------|-----------|-----------|-----------|-------|--------------------|-------|
| Series | Туре      | A(mm)    | B(mm)     | C(mm)     | D(mm)     | E(mm) | F(mm)              | G(mm) |
| HFZ    | 2012      | 2.0±0.20 | 1.25±0.20 | 0.85±0.20 | 0.50±0.30 | 1.05  | 1.00               | 1.45  |



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

### 6-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### 6-2.1 Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020F)

#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

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Fig.1 Soldering Reflow

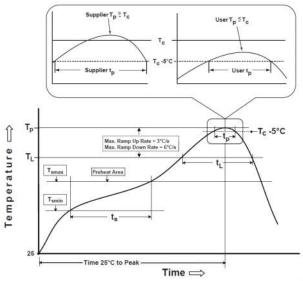
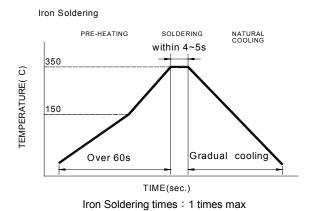


Fig.2 Iron soldering temperature profiles



Reflow times: 3 times max

Table (1.1): Reflow Profiles

| Profile Type:   | Pb-Free Assembly              |
|---|-------------------------------|
| Preheat -Temperature Min(T <sub>smin</sub> ) -Temperature Max(T <sub>smax</sub> ) -Time(t <sub>s</sub> )from(T <sub>smin</sub> to T <sub>smax</sub> ) | 150℃<br>200℃<br>60-120seconds |
| Ramp-up rate(T <sub>L</sub> to T <sub>p</sub> )   | 3℃/second max.                |
| Liquidus temperature(T <sub>L</sub> ) Time(t <sub>L</sub> )maintained above T <sub>L</sub>  | 217°C<br>60-150 seconds       |
| Classification temperature(T <sub>c</sub> )   | See Table (1.2)               |
| $\label{eq:total_power} \mbox{Time}(t_p) \mbox{ at Tc-} \mbox{ 5^{\circ}\!$     | < 30 seconds                  |
| Ramp-down rate(Tp to TL)  | 6℃ /second max.               |
| Time 25°C to peak temperature   | 8 minutes max.                |

 $\textbf{Tp}: \mbox{maximum peak package body temperature, } \textbf{Tc}: \mbox{the classification temperature.}$ 

For user (customer) **Tp** should be equal to or less than **Tc**.

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

|                  | Package<br>Thickness | Volume mm <sup>3</sup><br><350 | Volume mm <sup>3</sup><br>350-2000 | Volume mm <sup>3</sup> >2000 |
|------------------|----------------------|--------------------------------|------------------------------------|------------------------------|
|                  | <1.6mm               | 260°C                          | 260°C                              | 260°C                        |
| PB-Free Assembly | 1.6-2.5mm            | 260°C                          | 250°C                              | 245°C                        |
|                  | ≥2.5mm               | 250°C                          | 245°C                              | 245°C                        |

Reflow is referred to standard IPC/JEDEC J-STD-020F 。

### 6-2.3 Solder Volume:

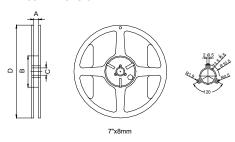
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



# 7. Packaging Information

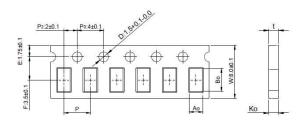
#### 7-1. Reel Dimension



| Туре   | A(mm)   | B(mm) | C(mm)    | D(mm) |  |
|--------|---------|-------|----------|-------|--|
| 7"x8mm | 9.0±0.5 | 60±2  | 13.5±0.5 | 178±2 |  |

#### 7-2.1 Tape Dimension / 8mm

#### ■Material of taping is paper

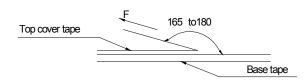


| Size   | Bo(mm)    | Ao(mm)    | Ko(mm)    | P(mm)    | t(mm)     |
|--------|-----------|-----------|-----------|----------|-----------|
| 201209 | 2.10±0.05 | 1.30±0.05 | 0.95±0.05 | 4.0±0.10 | 0.95±0.05 |

#### 7-3. Packaging Quantity

| Chip Size   | 201209 |
|-------------|--------|
| Chip / Reel | 4000   |
| Inner box   | 20000  |
| Middle box  | 100000 |
| Carton      | 200000 |

## 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

| Room Temp. | Room Humidity | Room atm | Tearing Speed |
|------------|---------------|----------|---------------|
| (℃)        | (%)           | (hPa)    | mm/min        |
| 5~35       | 45~85         | 860~1060 | 300           |

## **Application Notice**

- Storage Conditions(component level)
- To maintain the solder ability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than  $40\,^{\circ}\!\!\mathrm{C}$  and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.